

AKINADA

Results for III B.Tech I semester (R13/R10) Supplementary Examinations Nov-2019
College name: GAYATHRI VIDYA PARISHAD COLL OF ENGG FOR WOMEN, VISAKHAPATNA:JG

Htno	Subcode	Subname	Internal	External	CREDITS
12JG1A0208	R31022	ELECTRICAL MEASUREMENT	10	17	0
12JG1A0208	R31026	LINEAR & DIGITAL IC APPLICATION	12	17	0
12JG1A04A8	R31044	ELECTRONIC MEASUREMENTS AND INSTRUMENTAT	5	-1	0
13JG1A0252	RT31021	ELECTRICAL MEASUREMENTS	15	-1	0
13JG1A0252	RT31023	POWER SYSTEMS-II	15	8	0
13JG1A0252	RT31025	POWER ELECTRONICS	17	-1	0
13JG1A0252	RT31026	LINEAR & DIGITAL IC APPLICATIONS	14	-1	0
13JG1A0427	RT31045	ANTENNAS AND WAVE PROPAGATION	16	34	3
13JG1A0456	RT31043	CONTROL SYSTEMS	10	34	3
13JG1A0456	RT31045	ANTENNAS AND WAVE PROPAGATION	11	34	3
13JG1A0459	RT31045	ANTENNAS AND WAVE PROPAGATION	12	10	0
13JG1A0486	RT31044	DIGITAL SYSTEM DESIGN & DICA	15	8	0
13JG1A0486	RT31045	ANTENNAS AND WAVE PROPAGATION	11	-1	0
13JG1A1201	RT31055	OPERATING SYSTEMS	17	-1	0
14JG1A0435	RT31041	PULSE & DIGITAL CIRCUITS	16	19	0
14JG1A0473	RT31044	DIGITAL SYSTEM DESIGN & DICA	28	12	0
14JG1A0473	RT31045	ANTENNAS AND WAVE PROPAGATION	22	12	0
14JG1A04A5	RT31041	PULSE & DIGITAL CIRCUITS	17	18	0
14JG1A0531	RT31051	COMPILER DESIGN	15	7	0
14JG1A0531	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	14	0	0
14JG1A0531	RT31054	DATABASE MANAGEMENT SYSTEMS	15	18	0
14JG1A0531	RT31055	OPERATING SYSTEMS	18	22	0
14JG1A0532	RT31051	COMPILER DESIGN	12	-1	0
14JG1A0532	RT31052	DATA COMMUNICATION	18	-1	0
14JG1A0532	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	14	-1	0
14JG1A0532	RT31054	DATABASE MANAGEMENT SYSTEMS	7	-1	0
14JG1A0532	RT31055	OPERATING SYSTEMS	9	-1	0
14JG1A1213	RT31054	DATABASE MANAGEMENT SYSTEMS	12	33	3
14JG1A1213	RT31055	OPERATING SYSTEMS	10	30	3
14JG1A1213	RT31123	ADVANCED JAVA	17	35	3
14JG1A1224	RT31052	DATA COMMUNICATION	7	0	0
14JG1A1224	RT31054	DATABASE MANAGEMENT SYSTEMS	8	10	0
14JG1A1224	RT31121	SOFTWARE ENGINEERING	9	0	0
14JG1A1224	RT31128	DATABASE MANAGEMENT SYSTEMS LAB	14	21	2
14JG5A0418	RT31041	PULSE & DIGITAL CIRCUITS	8	-1	0
14JG5A0418	RT31043	CONTROL SYSTEMS	8	-1	0
15JG1A0205	RT31021	ELECTRICAL MEASUREMENTS	16	-1	0
15JG1A0205	RT31025	POWER ELECTRONICS	17	0	0
15JG1A0205	RT31026	LINEAR & DIGITAL IC APPLICATIONS	15	29	3
15JG1A0414	RT31042	LINEAR INTEGRATED CIRCUIT APPLICATIONS	12	9	0
15JG1A0414	RT31043	CONTROL SYSTEMS	8	33	3
15JG1A0414	RT31044	DIGITAL SYSTEM DESIGN & DICA	9	15	0
15JG1A0427	RT31016	IPR & PATENTS	10	-1	0
15JG1A0427	RT31041	PULSE & DIGITAL CIRCUITS	17	9	0
15JG1A0427	RT31044	DIGITAL SYSTEM DESIGN & DICA	4	-1	0

Htno	Subcode	Subname	Internal	External	CREDITS
15JG1A0427	RT31045	ANTENNAS AND WAVE PROPAGATION	7	8	0
15JG1A0450	RT31041	PULSE & DIGITAL CIRCUITS	12	18	0
15JG1A0474	RT31045	ANTENNAS AND WAVE PROPAGATION	15	14	0
15JG1A0475	RT31041	PULSE & DIGITAL CIRCUITS	6	10	0
15JG1A0475	RT31043	CONTROL SYSTEMS	16	45	3
15JG1A0475	RT31045	ANTENNAS AND WAVE PROPAGATION	13	10	0
15JG1A0479	RT31041	PULSE & DIGITAL CIRCUITS	17	29	3
15JG1A0479	RT31045	ANTENNAS AND WAVE PROPAGATION	11	29	3
15JG1A04A6	RT31045	ANTENNAS AND WAVE PROPAGATION	22	-1	0
15JG1A04B3	RT31043	CONTROL SYSTEMS	19	47	3
15JG1A0519	RT31051	COMPILER DESIGN	16	18	0
15JG1A0519	RT31055	OPERATING SYSTEMS	16	42	3
15JG1A0566	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	14	0	0
15JG1A0566	RT31054	DATABASE MANAGEMENT SYSTEMS	13	19	0
15JG1A05B8	RT31051	COMPILER DESIGN	16	0	0
15JG1A05B8	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	14	0	0
15JG1A05B8	RT31054	DATABASE MANAGEMENT SYSTEMS	9	8	0
15JG1A1209	RT31123	ADVANCED JAVA	12	27	0
15JG1A1216	RT31123	ADVANCED JAVA	9	24	0
15JG1A1228	RT31052	DATA COMMUNICATION	8	14	0
15JG1A1228	RT31054	DATABASE MANAGEMENT SYSTEMS	11	0	0
15JG1A1228	RT31055	OPERATING SYSTEMS	10	-1	0
15JG1A1228	RT31121	SOFTWARE ENGINEERING	9	-1	0
15JG1A1228	RT31123	ADVANCED JAVA	2	-1	0
15JG1A1228	RT31126	ADVANCED JAVA LAB	12	30	2
15JG1A1230	RT31055	OPERATING SYSTEMS	7	28	0
15JG1A1230	RT31121	SOFTWARE ENGINEERING	8	21	0
15JG1A1240	RT31055	OPERATING SYSTEMS	8	28	0
15JG5A0204	RT31024	ELECTRICAL MACHINES-III	18	-1	0
15JG5A0204	RT31026	LINEAR & DIGITAL IC APPLICATIONS	14	-1	0
16JG5A0211	RT31023	POWER SYSTEMS-II	20	-1	0
16JG5A0504	RT31051	COMPILER DESIGN	15	15	0

^{**}Note:1)[Last Date to apply for Recounting/Revaluation/Challenge Revaluation is: 20-01-2020]

Date:10.01.2020

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^{**} Note:**

^{* -1} in the filed of externals indicates student is absent for the respective subject.

^{* -2} in the filed of externals indicates student result Withheld for the respective subject.

^{* -3} in the filed of externals indicates student involved in Malpractice for the respective subject.